

Welcome To ICSL

Integrated Computer Systems Laboratory
School of Electrical Engineering, KAIST

Ask any Questions to Jeongmin Kim
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PROFESSOR



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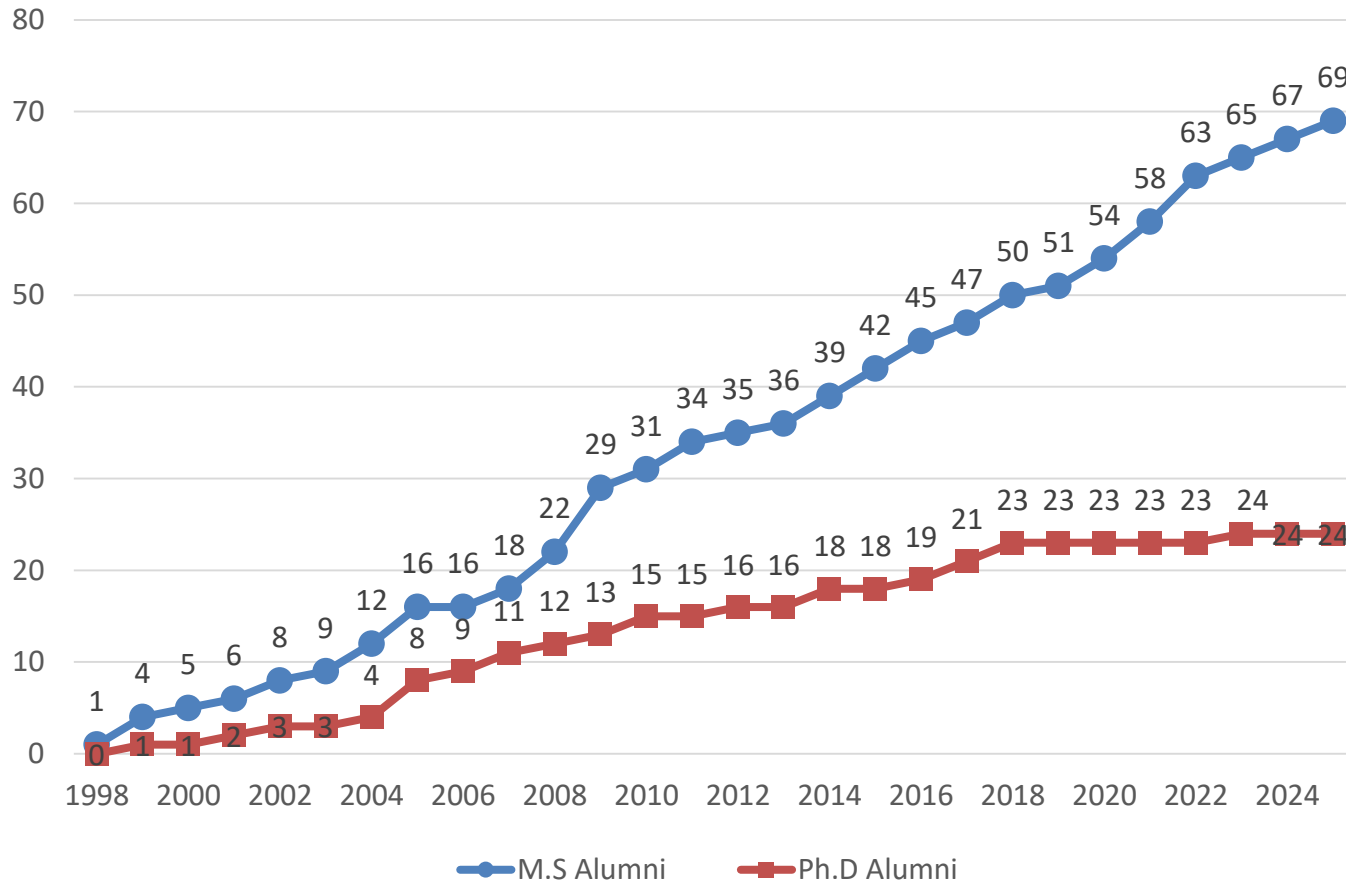
- ◆ B.S. in Seoul National Univ. (1986)
- ◆ M.S & Ph.D. in KAIST (1988, 1992)

- ◆ Full Professor in KAIST, EE
- ◆ IEEE Senior Member
- ◆ Associate Editor for IEEE Transactions on Circuits and Systems I:Regular Papers





MEMBERS



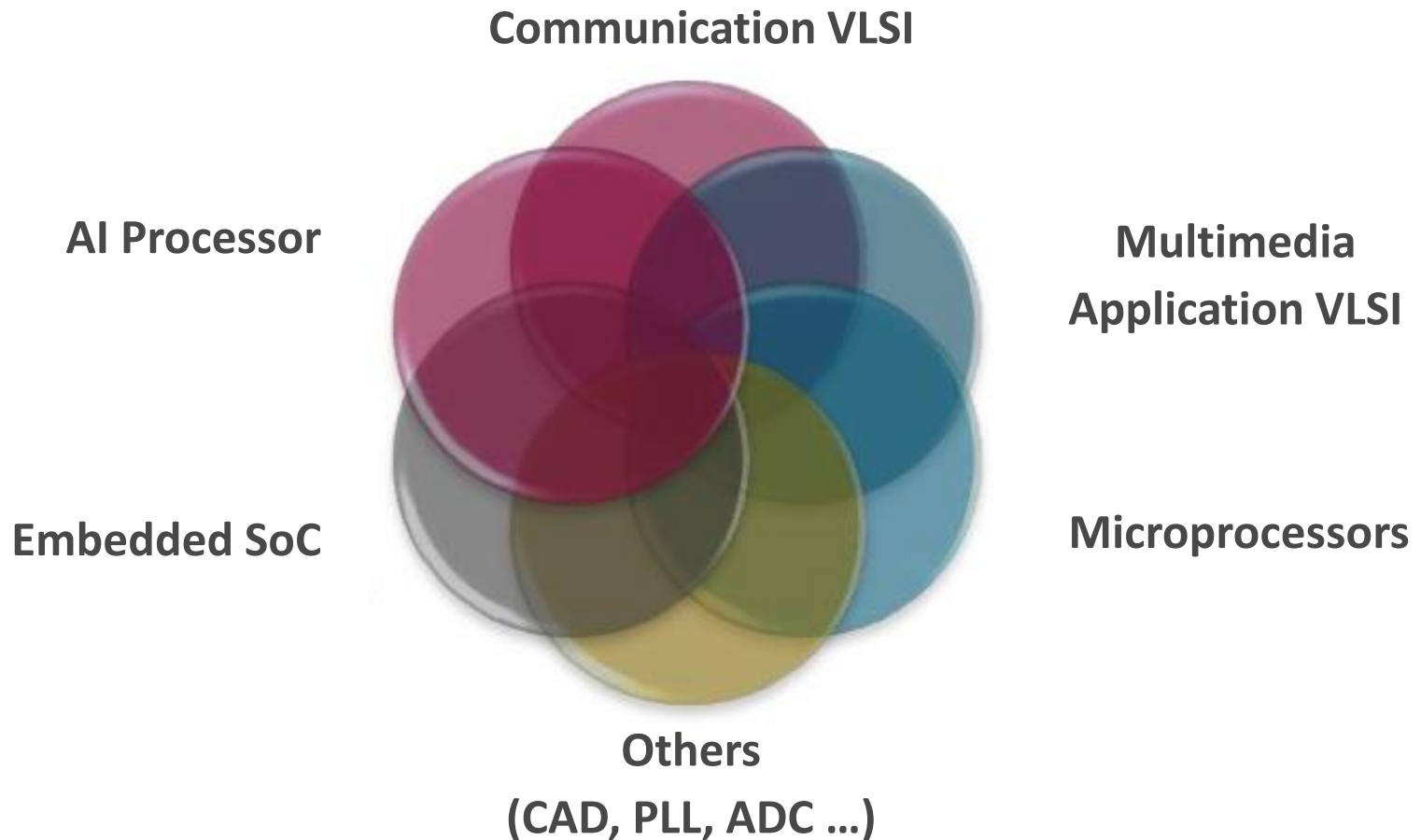
Currently in ICSSL, 3 PH.D Candidates and 11 M.S Candidates are working together :)



RESEARCH AREA

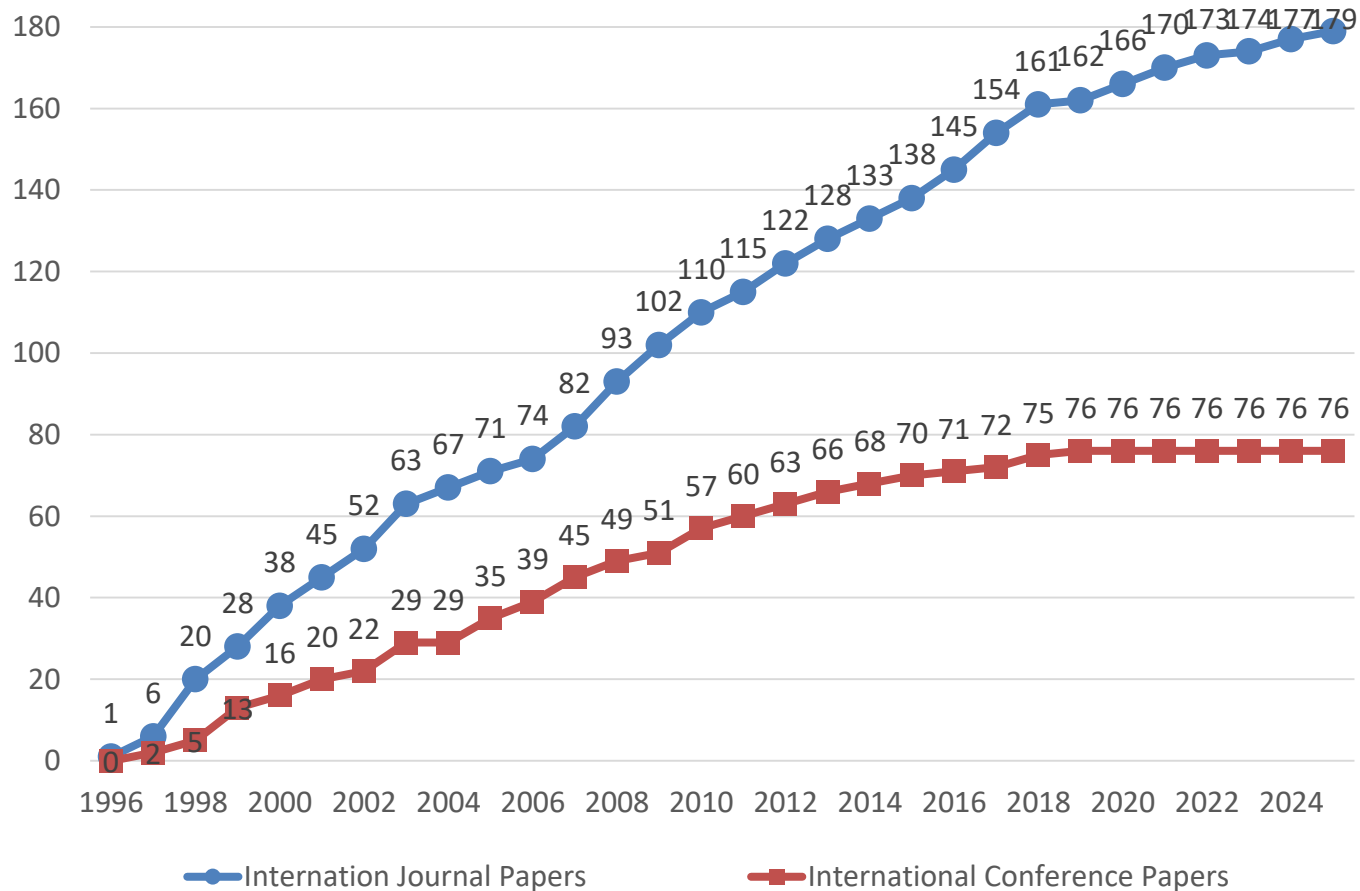


Circuits and Systems





ACADAMIC ACHIEVEMENTS



Currently in ICSL, We have 250 SCI journal and conference papers :)



LIFE IN ICSL



- ◆ Weekly Research Seminar
- ◆ Weekly Project Meeting
- ◆ Official Working Time : 9 AM ~ 6 PM
- ◆ 5 Working Days in a Week

- ◆ Fun Fun Fun !!!
 - Cherry blossom picnic
 - Strawberry party
 - Playing Basketball, Futsal, Tennis, ...
 - Summer / Winter workshops



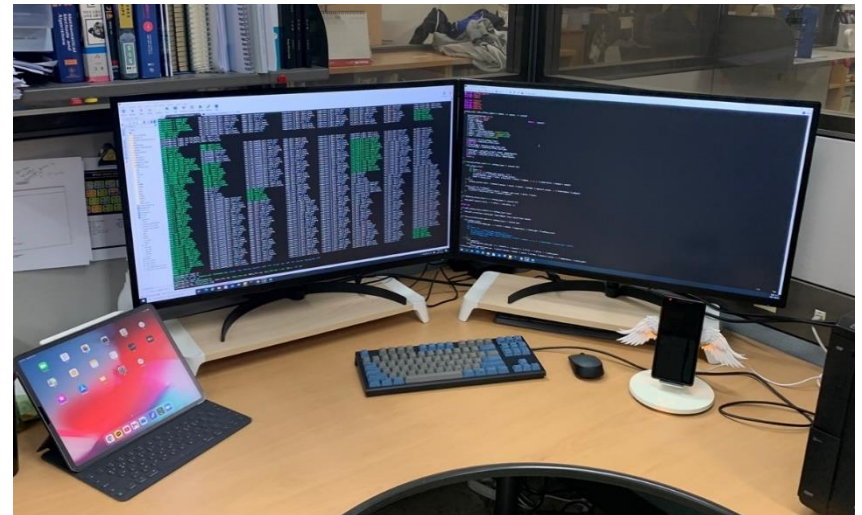


ENVIRONMENT



◆ 쾌적한 연구 환경

◆ 최신 개인 PC 및 주변 장치 제공





Happy ICSL

- *Feel free to contact ICSL*